

InstruTECH ITC-18

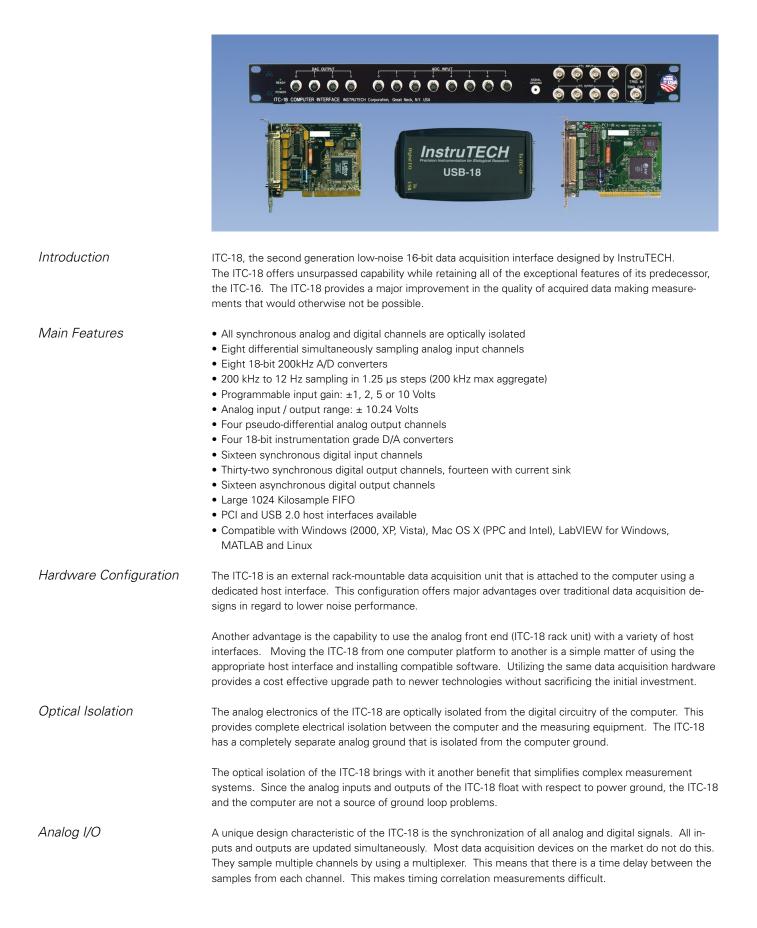
16-bit Multi-Channel Data Acquisition Interface





HEKA provides the finest instruments today to achieve the needed progress of tomorrow...





	 The ITC-18 provides eight analog input channels. Each input channel uses an 18-bit A/D converter preceded by a programmable gain stage. Each A/D input can be programmed with a full scale range of ±1, ±2, ±5, or ±10 Volts. This gain stage reduces the need for external signal amplifiers before the ITC-18. Using separate A/D converters for each analog input channel also has the advantage of no measurable cross talk between channels. The ITC-18 provides four 18-bit instrumentation grade D/A converters featuring high accuracy with less than 1 bit (~300 µV) of noise. This extremely low noise allows for the observation of least significant bit transitions. The D/A circuitry used in the ITC-18 is temperature stabilized and "De-glitched" for ideal performance. This architecture makes the ITC-18 particularly important when making measurements using high-frequency signal sources, multiple signal sources with large differences in amplitude between them or when analyzing multiple signals for timing correlation. 		
Digital I/O	The ITC-18 provides sixteen digital inputs, thirty-two digital outputs, seven sequence RAM trigger outputs and fourteen asynchronous non-isolated digital outputs. The digital input channels feature level sensitive or latched modes. For maximum versatility the inputs can be inverted allowing rising or falling edge trig- gering. Thirty-two digital output channels in two banks of sixteen, with fourteen channels paralleled with current sink circuitry, for driving perfusion valves or other devices directly.		
FIFO Memory	The ITC-18 is supplied with a large 1024 Kilosample FIFO memory for input and output data. A large FIFO memory allows uninterrupted continuous hi-speed acquisition with today's multitasking operating systems.		
Specialized Applications	The ITC-18 uses programmable gate arrays that allow the internal hardware to be altered for specialized applications. One available application is the <i>Artificial Synapse Dynamic Clamp</i> .		
Software Control * All drivers are available for download.	Mac OS X (PPC & Intel)*2000, XP, Vista*Linux*• Driver and Framework Library• Kernel Driver and DLL Library• Driver and C/C++ Library• WaveMetrics IGOR Pro XOPs• WaveMetrics IGOR Pro XOPs• Driver and C/C++ Library• PATCHMASTER, CHARTMASTER• National Instruments LabVIEW• Patchmaster, Chartmaster, Pulse, X-chart, Tida• InstruTECH AcquireT• InstruTECH AcquireT• InstruTECH AcquireT and Ecell • Strathclyde WinWCP • AxoGraphX• AxoGraphX		
Host Interfaces	USB-18USB v2.0 high-speed (480 Mbps) interfacePCI-18short, 32-bit, 33 MHz, 5 Volt PCI bus masteringPCI-18v3short, 32-bit, 33 MHz, 3.3 Volt PCI bus masteringA note about Power Mac G5 computers: Apple has once again changed the PCI expansion slots on their G5 desktop computers. The latest release, as of November 2005, has PCI Express bus architecture. This is also true for the Intel-based Mac Pro desktop computers. The PCI-18 and PCI-18V3 cards are NOT com- patible with the PCI Express bus. For these new systems, the USB-18 host interface is available. Power Mac G5 computers before November 2005 used PCI-X expansion slots. These systems are com- patible with either the PCI-18V3 or the USB-18 host interface.		
Product Content	 One rack mounted ITC-18 interface unit One host interface (choice of PCI-18, PCI-18v3, USB-18) One printed manual Cables to connect the ITC-18 to the host interface and the PC and to the power line <i>Item No.:</i> <i>ITC-18/PCI-18 (ITC-18 with PCI-18 for 5V PCI bus)</i> <i>ITC-18/PVI-18v3 (ITC-18 with PCI-18v3 for 3.3V PCI bus)</i> <i>ITC-18/USB-18 (ITC-18 with USB-18 for USB 2.0)</i> 		



Technical Specifications

Analog Input:

Number of channels Type of ADC Input connector Resolution 16-bit data Acquisition rate Input range Aperture delay Aperture delay Aperture jitter Conversion speed Differential nonlinearity Drift Input impedance Signal-to-noise ratio Crosstalk

8, differential, optically isolated successive approximation BNC 18-bit converter (1 in 65536) 200 kHz aggregate -10.24 to +10.239 Volts 10 ns 50 ps rms software selectable, 5 μs min ±0.002% of FSR ±50 ppm/ °C 1 M_ +90 dB <1 LSB

Programmable Gain: Gain

Settling time Nonlinearity Max. input voltage Input CMRR

Digital Inputs:

Digital inputs Input type Operational mode

Minimum pulse width Input connectors

Max. input voltage

Trigger Input:

Number

Input type Operational mode

Minimum pulse width Input connector Max. input voltage

Analog Output: Number of channels

Type of DAC Output connector Resolution

Output range Conversion speed Gain error Gain linearity Gain drift Signal-to-noise ratio software selectable 1, 2, 5 or 10 V/V 3.5 μs to 0.01% all gains ±0.0003% of FSR ±40 Volts 100 dB

16, logic level, optically isolated CMOS logic compatible software selectable level sense / latching active high / active low 150 ns bits (0 to3) on front panel BNC 16 bits on rear panel connector ±40 Volts

1, hardware selectable isolated / non-isolated CMOS logic compatible software selectable edge mode and invert sense 150 ns BNC on front panel ±40 Volts

4, pseudo-differential optically isolated double buffered, multiplying BNC 18-bit converter 16-bit data (1 in 65536) -10.24 to +10.239 Volts software selectable, 5μs max 0.2% of FSR <2 dB ±25 ppm of FSR/ °C 116 dB Output impedance

Short circuit to ground Output load current

Trigger Output:

Number

Output driver

Output connector Max. output current

Digital Outputs:

Standard Number Output driver

Output connectors

High current drive Number Output driver

Output connector Output sink current *Asynchronous outputs* Number Output driver Output connector *Sequencer outputs* Number Output driver

Output connector

FIFO Memory: Standard

Power Requirements:

Input Voltage Input Frequency Maximum Power

Dimensions:

Width Height Depth Weight

Warranty:

Duration

200 _ (for output overload pro tection) indefinite 8 mA typical

1, selectable isolated / non-isolated AC, HCT, ACT, HCT, VCT, or 8 TTL loads BNC on front panel 6 mA

32, optically isolated AC, HCT, ACT, HCT, VCT, or 8 TTL loads bits (0 to 3) on front panel BNC 32 bits on dual 50 pin connector

14, optically isolated AC, HCT, ACT, HCT, VCT, or 8 TTL loads 14 bits on DB-25 pin connector 350 mA maximum

16, non-isolatedHC logic compatible34 pin connector

7, optically isolated AC, HCT, ACT, HCT, VCT, or 8 TTL loads 7 bits on dual 50 pin connector

1024 kilo sample FIFO

85-264 VAC 47-440 Hz 15 Watts

47.5 cm (19 inches) 4.375 cm (1.75 inches) 26.25 cm (10.5 inches) 3.6 kg (8 pounds)

One year parts and labor

support@heka.com



HEKA Elektronik Dr. Schulze GmbH Wiesenstraße 71 D-67466 Lambrecht/Pfalz Germany	Phone Fax Web Site Email	+49 (0) 63 25 / 95 53-0 +49 (0) 63 25 / 95 53-50 http://www.heka.com sales@heka.com support@heka.com
HEKA Electronics Incorporated 47 Keddy Bridge Road R.R. #2 Mahone Bay, NS B0J 2E0 Canada	Phone Fax Web Site Email	+1 902 624 0606 +1 902 624 0310 http://www.heka.com nasales@heka.com support@heka.com
HEKA Instruments Inc. 2128 Bellmore Avenue Bellmore, New York 11710-5606 USA	Phone Fax Web Site Email	+1 516 882 1155 +1 516 467 3125 http://www.heka.com ussales@heka.com

General notice:

Product names used herein are for identification purposes only and may be trademarks of their respective owners. HEKA disclaims any and all rights in those marks.

We reserve the right to effect technical changes as development progresses. Special versions are available on request. Further technical data are provided by a detailed description, which is available on request. A warranty of one year applies on all instruments.